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Block-level added redundancy explicit authentication for parallelized encryption and integrit processor-memory transactions

Recuven Staz, Lionel Yorres, Gilles Sassatelli, Pierre Guillemin, Michel Bardouillet, Albert Martinez January 2010 Transactions on computational science X

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The bus between the System on Chip (SoC) and the external memory is one of the weakest poin systems; an adversary can easily probe this bus in order to read private data (data confidentialit inject data (data integrity concern). .

Keywords: authentication, computer security, data integrity and confidentiality, encryption, harmemory

2 Cognitive agents based authentication & privacy scheme for mobile transactions (CABAPS) B. Sathish Babu, P. Venkats:am November 2008

Computer Communications, Volume 31 Issue 17 Publisher: Elsevier Science Publishers B. V.

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Maintaining anonymity during authentication has become challenging research issue in the field c The application level authentication schemes have seriously affected by the absence of effective protect the privacy of a ...

Keywords: Authentication, Cognitive agents, Mobile security, Mobile transactions, Privacy

3 An energy efficient security protocol for IEEE 802.11 WLANs

Phongsak Keeratiwintskorn, Prashant Krishnamurthy

April 2006 Pervasive and Mobile Computing , Volume 2 Issue 2

Publisher: Elsevier Science Publishers B. V.

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and several message exchanges for authenticated key exchange at the session level and data co integrity at the packet level. ...

Keywords: Cryptography, Energy efficiency, IEEE 802.11, Security, Wireless networks

- 4 MPCS: a secure Mobile Payment Consortia System for higher educational institutions
- S. Britto H. Kumar, S. Albert Babara, J. Bonal Martin November 2009 ICIS '09: Proceedings of the 2nd International Conference on Interaction Sciences Technology, Culture and Human

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Keywords: framework, mobile payment, mobile payment consortia, secured transactions, softw